JAN 2 2 2003 SEE

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of: VINSON ET AL.

Serial No. 09/915,762

Filing Date: July 26, 2001

Eor: DECOUPLING CAPACITOR CLOSELY

COUPLED WITH INTEGRATED

CIRCUIT

#9/ Election

Examiner: T. Dinh

) Art Unit: 2827

SECOND RESPONSE TO REQUIREMENT FOR RESTRICTION

Director, U.S. Patent and Trademark Office Washington, DC 20231

Sir:

Applicants filed an initial Response to Restriction Requirement of October 1, 2002, electing Group I, Claims 1-31, drawn to an integrated chip module and multi-chip module, classified in class 361, subclass 782.

On December 31, 2002, the Examiner mailed a detailed action requesting that Applicants supply a correction by electing a single disclosed species for prosecution on the merits, i.e., either Specie I relative to FIG. 4: without a thin film metallization layer positioned on the capacitor carrier; or Specie II relative to FIG. 4A: with a thin film metallization layer positioned on the capacitor carrier.

Applicants elect Species I directed to FIG. 4: without a thin film metallization layer positioned on the capacitor carrier.

To Patent Application of: VINSON ET AL. Serial No. 09/915,762

Filing Date: July 26, 2001

If the Examiner has any questions concerning this response, the undersigned attorney would appreciate a telephone call.

Respectfully submitted,

RICHARD K. WARTHER
Reg. No. 32,180
Allen, Dyer, Doppelt, Milbrath
& Gilchrist, P.A.
255 S. Orange Avenue
Suite 1401
Post Office Box 3791

Post Office Box 3791 Orlando, Florida 32802 407/841-2330

CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: DIRECTOR, U.S. PATENT AND TRADEMARK OFFICE, WASHINGTON, DC 20231, on this ________ day of January, 2003.